# Freeform Search

Database:	US Pre-Grant Publication Full-Text Database US Patents Full-Text Database US OCR Full-Text Database EPO Abstracts Database JPO Abstracts Database Derwent World Patents Index IBM Technical Disclosure Bulletins			
Term:	L17 and 13 回			
Display:	10 Documents in <u>Display Format</u> : - Starting with Number 1			
Generate:	○ Hit List   Hit Count ○ Side by Side ○ Image			
Search Clear Interrupt				
Search History				

## DATE: Thursday, December 09, 2004 Printable Copy Create Case

Set Name side by side	Query	Hit Count	Set Name result set
DB=	PGPB,USPT,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=ADJ		
<u>L18</u>	L17 and 13	6	<u>L18</u>
<u>L17</u>	L16 and l15	29	<u>L17</u>
<u>L16</u>	ll adj4 (transmit or transfer or transmitting or transferring or receive or receiving or write or writing or read or reading or send or sending) adj4 (data or information)	2007	<u>L16</u>
<u>L15</u>	12 same (access or accessing) same register	472	<u>L15</u>
<u>L14</u>	L13 and l3	72	<u>L14</u>
<u>L13</u>	(serial and interface).ti.	2451	<u>L13</u>
<u>L12</u>	(serial and interface.ti,ab.)	30020	<u>L12</u>
<u>L11</u>	12 adj2 ("and") adj2 11	0	<u>L11</u>
<u>L10</u>	L9 and 18	11	<u>L10</u>
<u>L9</u>	l4 adj4 l1	1644	<u>L9</u>
<u>L8</u>	13 adj4 11	351	<u>L8</u>
<u>L7</u>	L6 and 15	5	<u>L7</u>
<u>L6</u>	710/???.ccls.	11043	<u>L6</u>

<u>L5</u>	11 same 12 same 13 same 14	150	<u>L5</u>
<u>L4</u>	Host or cpu or motherboard	703199	<u>L4</u>
<u>L3</u>	network adj2 (interface or card)	68161	<u>L3</u>
<u>L2</u>	parallel adj2 (interface or port)	27845	<u>L2</u>
<u>L1</u>	serial adj2 (interface or port)	48875	<u>L1</u>

### END OF SEARCH HISTORY

### **Refine Search**

#### Search Results -

Term	Documents
EMBED	57635
EMBEDS	8880
EMBEDDING	70238
EMBEDDINGS	165
ENCODE	151668
ENCODES	106600
ENCODING	278228
ENCODINGS	4237
ENCAPSULATE	46106
ENCAPSULATES	21094
ENCAPSULATING	66713
((EMBED OR EMBEDDING OR ENCODE OR ENCODING OR ENCAPSULATE OR ENCAPSULATING) SAME L7 SAME (WRITE) ).PGPB,USPT,EPAB,JPAB,DWPI,TDBD.	248

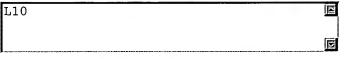
There are more results than shown above. Click here to view the entire set.

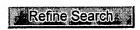
US Pre-Grant Publication Full-Text Database
US Patents Full-Text Database
US OCR Full-Text Database

Database:

EPO Abstracts Database
JPO Abstracts Database
Derwent World Patents Index
IBM Technical Disclosure Bulletins

Search:











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Set Query Name side by

<u>Set</u>
<u>Hit</u> <u>Name</u>
<u>Count</u> result

side				set
DB=	PGPB, USPT, EPAB, JPAB, DWPI, TDBD; PLUR=YES; OP=ADJ			
<u>L10</u>	(embed or embedding or encode or encoding or encapsulate or encapsulating) same 17 same (write)		248	<u>L10</u>
<u>L9</u>	(embed or embedding or encode or encoding or encapsulate or encapsulating) same 17 same (write adj2 command)		2	<u>L9</u>
<u>L8</u>	(embed or embedding or encode or encoding or encapsulate or encapsulating) adj4 17 adj4 (write adj2 command)		0	<u>L8</u>
<u>L7</u>	(data adj2 structure)	87	7781	<u>L7</u>
<u>L6</u>	L5 and l4		9	<u>L6</u>
<u>L5</u>	serial adj2 (link or port or interface)	54	1764	<u>L5</u>
<u>L4</u>	(encode or encapsulate or encapsulating or encoding or embed or embedding) adj4 (write adj2 command)		21	<u>L4</u>
<u>L3</u>	(encode or encapsulate or encapsulating or encoding or embed or embedding) same (write adj2 command) same (format or formatting)		21	<u>L3</u>
<u>L2</u>	host and 11		1	<u>L2</u>
L1	6275501.pn.		2	<u>L1</u>

### END OF SEARCH HISTORY

## Freeform Search

Database:	US Pre-Grant Publication Full-Text Database US Patents Full-Text Database US OCR Full-Text Database EPO Abstracts Database JPO Abstracts Database Derwent World Patents Index IBM Technical Disclosure Bulletins			
Term:	controller adj4 (implement or implementing) adj4  ASIC			
Display:	10 Documents in Display Format: - Starting with Number 1			
Generate: ○ Hit List   Hit Count ○ Side by Side ○ Image				
Search Clear Interrupt				
Search History				

DATE: Thursday, December 09, 2004 Printable Copy Create Case

Set Name Query H		<b>Hit Count S</b>	<u>Set Name</u>
side by side			result set
DB=Pc	GPB, $USPT$ , $EPAB$ , $JPAB$ , $DWPI$ , $TDBD$ ; $PLUR = YES$ ; $OP = AB$	ADJ	
<u>L5</u>	controller adj4 (implement or implementing) adj4 ASIC	3	<u>L5</u>
<u>L4</u>	controller adj4 (implement or implementing) adj4 FPGA	0	<u>L4</u>
<u>L3</u>	(controller adj4 FPGA) same advantages	. 19	<u>L3</u>
<u>L2</u>	controller adj4 FPGA	482	<u>L2</u>
<u>L1</u>	controller same FPGA	3464	<u>L1</u>

END OF SEARCH HISTORY